

- in claim 42, line 4, add --- simultaneously digitally processing the first and second radio audio signals to generate respective first and second digitally processed audio signals;-----.

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1/25/08

- in claim 42, line ⁵⁻⁶~~8~~, delete ---generating respective first and second digitally processed audio signal based on the respective first and second radio tuner audio signals;-----.

REASON FOR ALLOWANCE

4. Claims 1-4, 6, 8-9, 11-12, 15-18, 20-22, 26-27, 29-30, 35, and 42-46 are allowable over the cited prior art.

5. The examiner's amendment above is made to put the claims in condition for allowance.

The following is an examiner's statement of reasons for allowance:

The cited prior art, Kelley (JP 05-183461), discloses one tuner with different station selections whose outputs are multiplexed and then demultiplexed for input into accumulators and a digital processing system for demodulation.

Utsunomiya (JP 06-204926) discloses a double tuner diversity RDS receiver wherein a microcomputer chooses a better tuner via a switch in response to the data obtained from the RDS data demodulators, PLL circuits, and the detectors.